# Partitioned Communication to Support Hybrid Programming

PRESENTER: DR. RYAN E. GRANT

STUDENT CREDIT: YILTAN TEMUCIN, JORDAN ABT, ELIZABETH REID





# Hybrid Programming

- •More than just MPI+X in the traditional sense
	- Not just MPI+OpenMP
- •Support for large amounts of concurrency
- •Not necessarily on the CPU-only
	- Need solutions for accelerators as well
- •Let's examine how we got here and what we need going forward and how Partitioned communication let's us get there….





# Why Partitioned? Why Now?

MPI use cases continue to evolve

◦ MPI+X implies the use of threads, e.g. OpenMP

High Bandwidth Memory (HBM) plays a role

- Faster memory built on-die/in-package but you get less capacity than previously
- Buy DRAM for channels and get a lot of space you can use, not so with HBM
- MPI processes can take a lot of memory each

•Core counts skyrocketing

- 192 cores in an AMD EPYC, 384 in a dual-socket node
- Remember 56-core used to be a many-core architecture?

•Accelerators

• Can't rely on CPUs to move your messages anymore, different architectures, new challenges





### What do we want in a solution?

Desirable/required features:

Low overhead

◦ Having many messages and ranks causes message matching/steering overhead

Similar semantics to existing concurrency solutions

◦ Ease of programmability

Decouple message passing setup/handling from data movement

- Don't need to setup at the time of sending
- Useful for accelerators

Minimal locking/sycnhronization





# Why not thread multiple?

Threads introduce significant issues with concurrency in existing MPI implementations

- MPI\_THREAD\_MULTIPLE is hard and implementations don't do it well
- Difficult to support concurrency without encountering conflicts

Need to expose the parallelism but without locking/synchronization

- Existing solutions already provide a way
- Build without conflicts between execution context (threads) and you don't need to worry about locking





#### Concurrency costs

Have known about matching list performance for sometime

Non-viable with many many messages, so you can either avoid many messages that need matching or reduce costs of matching

Partitioned avoids the matching outside of init and avoids wildcards to let other matching happen in efficient manners (e.g. hashing)



Data from Measuring Multithreaded Message Matching Misery, EuroPar 2018, Whit Schonbein et al.<br>Data from Measuring Multithreaded Message Matching Misery, EuroPar 2018, Whit Schonbein et al.







#### Concurrency costs



 $\overline{32}$ 

 $64$ 

 $\frac{128}{ }$ 

#### But concurrency helps



for P processes and T threads per process on an Intel Phi

Takeaway:Fewer threads=better performance, due to poor MPI multi-threading

10000es Dinan, Ryan E. Grant, Pavan Balaji, Dave Goodell, Douglas Miller, Marc Snir, Rajeev Thakur, "Enabling @000nication concurrency through flexible MPI Endpoints", International Journal of High Performance Computing Applications, Volume 28, Issue 4, pp. 390-405, November 2014. Impact factor: 1.63





# Partitioned Communication





# MPI Partitioned Communication Concepts

Many actors (threads) contributing to a larger operation in MPI

- Same number of messages as today!
- No new ranks no need to understand target thread

Many threads work together to assemble a message

- MPI only has to manage knowing when completion happens
- These are actor/action counts, not thread level collectives

Persistent-style communication

◦ Init…(Start…test/wait)…free

No heavy MPI thread concurrency handling required

◦ Leave the placement/management of the data to the user

No more complicated packing of data, send structures when they become available





#### How to use Partitioned MPI

Like persistent communications, setup the operation int MPI\_Partitioned\_send\_init(void \*buf, int partitions, int count, MPI\_Datatype data\_type, int to\_rank, int to\_tag, int num\_partitions, MPI\_Info info, MPI\_Comm comm, MPI\_Request \*request)

Start the request int MPI Start(MPI\_Request request)

Add items to the buffer

int MPI\_Pready(int partition, MPI\_Request request) MPI Pready is thread-safe and meant to be called from separate threads

Wait on completion int MPI\_Wait(MPI\_Request request)

Optional: Use the same partitioned send over again int MPI\_ Start(MPI\_Request request)





#### Persistent Partitioned Buffers

Expose the "ownership" of a buffer as shared to MPI

Need to specify the operation to be performed before contributing segments

MPI implementation doesn't have to care about sharing

◦ Only needs to know how many times it will be called

Applications are required to manage thread buffer ownership such that the buffer is valid

◦ The same as would be done today for codes where many threads work on a dataset (with the exception of reductions)

Result: MPI is thread agnostic with a minimal synchronization overhead (atomic\_fetch\_and\_add)

◦ Can alternatively use a task model instead of threads, IOVEC instead of contiguous buffer





New Type of Overlap

"Early bird communication"

Early threads can start moving data right away

Can implement using RDMA to avoid message matching







#### GPUs and Accelerators

GPUs cannot run MPI libraries natively – at least not well

Need coordination for network transfers

◦ But don't want to setup communications from step one on the GPU/Accelerator

Minimize overhead of communication initialization

◦ Many potential notifications – must be lightweight

Existing NIC hardware can use triggering ◦ Need a mechanism in MPI to do lightweight triggering

Communication can be optimized by host CPU

- CPU can optimize to network before the transfer takes place
- Optimize number of transfers, when things trigger





# Allow for Better Parallelism in MPI

Concept of many actors (threads/warps/thread blocks) contributing to a larger operation in MPI

- Same number of messages as you use in isend/irecv today
- No new ranks

Many threads work together to assemble a message

- MPI only has to manage knowing when completion happens
- These are actor/action counts, not thread level collectives, to better enable tasking models

No heavy MPI thread concurrency handling required

- Leave the placement/management of the data to the user
- Knowledge required: number of workers, which is easily available

Bonus: Match well with Offloaded NIC capabilities

- Use counters for sending/receiving
- Utilize triggered operations to offload sends to the NIC





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◦ Only needs to understand how many times it will be called

Threads are required to manage their own buffer ownership such that the buffer is valid

◦ The same as would be done today for code that has many threads working on a dataset (that's not a reduction)

Result: MPI is thread agnostic with a minimal synchronization overhead (atomic\_inc)

◦ Can alternatively use task model instead of threads, IOVEC instead of contiguous buffer





#### Example from MPI-4.0







# Opportunities for Optimization

MPI implementations can optimize data transfer under the covers: 2.1 A Solution for Many Applications of Many Applicati

- **Existing multim-sand send data** subdivide larger buffers and send data many threads (actors) contribute to a single solution in a when ready bullet when ready
- $\overline{a}$   $\overline{b}$   $\overline{c}$  and  $\overline{b}$  is a continuous determined the base of  $\overline{c}$ ■ Could be optimized to specific networks each thread being of primary concerns of primary concerns of primary concerns of  $M$  $t_{\text{init}}$  is the state in MPI is concomitation in MPI is concluded in MPI is concluded in  $\mathcal{L}$
- $\blacksquare$  Number of messages will be and the concernation is the solution is the main concern. **• Number of messages will be:**

 $1 <$ #messages  $\leq$ #threads/tasks  $\mathbf{r}$  the simulation space with a given MPI process in the simulation  $\mathbf{r}$ For a partition with 1 part per thread

 $\blacksquare$  Doduces the total number of  $\sim$ **Reduces the total number of messages** for the processing matching over  $\sim$  the data threads normally the threads the threads threads the threads threads the threads threads threads threads threads threads the threads th sent, decreasing matching overheads

















#### Hardware adds more parallelism

More device contexts to separate traffic

◦ More hardware to use for sending data independently

More paths through the network

◦ Leverage the network resources more than single path routing

More options for direct accelerator messaging

◦ Build in pready support to GPUs for triggering





#### CPU Application Benefit absorb all of the other-than one of communication is communicated on  $\mathbb{R}^n$ wise be amortized away by repeating communications to neighbors

Real reactor physics proxy app: SimpleMOC







In this work, we have introduced the partitioned send approach

# GPU-initiated Partitioned





### Example

Adding in GPUs, we have similar flow to CPU only partitioned

Pready/arrived are implemented in GPU space

Can bypass CPU for communication



Credit: Yiltan Temucin et al. To appear ExaMPI





#### Usage model - Kernel communication triggering



Note: CPU does communication setup and completion steps for MPI. Setup commands on NIC and poll for completion of entire operation. Kernel just indicates when NIC/MPI can send data. Ideally want to trigger communication from GPU to fire off when data is ready without communication setup/completion in kernel





#### One way to do it  $-$  with extras

MPIX\_Device **int** MPIX\_Pready(**int** partition, MPIX\_Prequest preq);

\_\_global\_\_ **int** kernel\_B(MPIX\_Prequest preq, **double** \*sbuf)

{

```
int idx = threadIdx.x + blockDim.x * threadIdx.y; /* Do Work */
MPIX Pready(idx, preq);
```
} \_\_host\_\_ **int** host\_function(MPI\_Request req,

MPI\_Start(req)

**double** \*sbuf)

{ MPIX\_Pbuf\_Prepare(req);

**if** (first iteration) {MPIX\_Prequest\_create(preq, req); }

kernel\_B<<<stream>>>(preq, sbuf);

/\* Do work on host \*/

MPI\_Wait(req); }





#### Wait, what are those MPIX calls?

- We need to be able to define what requests look like on a GPU
- GPU MPI\_Request may not equal CPU-side MPI request struct
- Why? Much better ways to lay out data for GPUs to use efficiently than CPU-type structures
- We also need the GPU to have requests that are already setup for it ◦ Helps with performance
- Is this what will be in future versions?
- No, we will define the needs in bindings for the kernel environments





### Avoiding synchronization

Synchronization is costly, so Partitioned avoids this by creating logical separation on data movement based on the partition numbering

See that simple vector addition kernel with synchronization happening grows in cost as grid size grows but synchronization costs remain the same

Avoid this by just not having to synchronize



Inefficient region to sync





### Performance Benefits

#### Example: Jacobi solver

8-GPUs

We can see that not synchronizing is helping in the smaller ranges, we're seeing a combination of not synchronizing with early bird communication

Results scale up better with increasing node counts







#### The Future

No need to stop at point-to-point

Partitioned collectives show promise, here is first example of partitioned collectives prototype running with GPU-initiated communication

Can see that things are much better but still not at vendor proprietary levels







# Still plenty to be done

As shown with previous presentation, libraries are just deploying Partitioned, more tuning needs to be done

◦ Many possible new optimizations, we are just scratching the surface with recent ideas, plenty more to come

#### Adoption path still in front of us

- Applications can take advantage of partitioned communication
- Especially GPU applications with native bindings
- Writing bindings for GPUs
- Side document to MPI





# Thank You!

# Questions?



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